

In the Claims

Please amend Claims 11-12 and 15-18. Amendments to the claims are indicated in the attached "Marked Up Version of Amendments" (pages i - ii).

- Sub C1 cont.*
11. (Amended) A method as claimed in Claim 6 wherein the phase of the edge of the input signal is directly compared with the phase of the edge of the output signal in combinational circuitry having an output which depends only on the state of an input to the combinational circuitry.
12. (Amended) A method as claimed in Claim 6 wherein the phase comparator produces up and down pulses which, when the phase of the edge of the input signal is aligned with the phase of the edge of the output signal, each have a duration which is a fraction of the input signal and the output signal transition times.
- Sub C2 cont.*
15. (Amended) A multiplier circuit as claimed in Claim 1 wherein the phase of the edge of the input signal is directly compared with the phase of the edge of the output signal in combinational circuitry having an output which depends only on the state of an input to the combinational circuitry.
- Sub C2*
16. (Amended) A multiplier circuit as claimed in Claim 1 wherein the phase comparator produces up and down pulses which, when the phase of the edge of the input signal is aligned with the phase of the edge of the output signal, each have a duration which is a fraction of the input signal and the output signal transition times.
17. (Amended) A multiplier circuit as claimed in Claim 1 wherein the phase of the edge of the input signal is directly compared with the phase of the edge of the